

What is claimed is:

1. A liquid crystal display substrate, comprising:
 - a substrate which sandwiches a liquid crystal having negative dielectric anisotropy in combination with an opposite substrate provided in a face-to-face relationship therewith;
 - a plurality of gate bus lines formed on the substrate;
 - a plurality of drain bus lines formed on the substrate such that they intersect the gate bus lines;
 - pixel regions defined by the gate buslines and the drain bus lines;
 - a thin film transistor formed in each of the pixel regions;
 - a resin color filter layer formed in each of the pixel regions;
 - a pixel electrode formed in each of the pixel regions;
 - and
 - an alignment regulating structure formed on the substrate for regulating the alignment of the liquid crystal.
2. A liquid crystal display substrate according to claim 1, further comprising an opaque film for shielding edges of the pixel regions from light.
3. A liquid crystal display substrate according to claim 2, wherein the opaque film is formed by laminating the resin color filter layers.
4. A liquid crystal display substrate according to claim 1,

wherein the pixel electrode is formed on the resin color filter layer.

5. A liquid crystal display substrate according to claim 4, wherein the pixel electrode is formed such that the electrode overlaps the drain bus lines when viewed in a direction perpendicular to a surface of the substrate.

6. A liquid crystal display substrate according to claim 4, wherein the pixel electrode is formed such that the electrode does not overlap the drain bus lines when viewed in a direction perpendicular to a surface of the substrate.

7. A liquid crystal display substrate according to claim 1, wherein the alignment regulating structure is a linear protrusion.

8. A liquid crystal display substrate according to claim 1, further comprising a columnar spacer for maintaining a cell gap, the columnar spacer being formed by laminating resin layers formed on the substrate.

9. A liquid crystal display substrate according to claim 8, wherein the resin layers include the resin color filter layers.

10. A liquid crystal display substrate according to claim 8, wherein the resin layers include a black resin layer.

11. A liquid crystal display substrate according to claim 8,

wherein the resin layers include a layer in which the linear protrusion is formed.

12. A liquid crystal display substrate, comprising:

a substrate which sandwiches a liquid crystal having negative dielectric anisotropy in combination with an opposite substrate provided in a face-to-face relationship therewith;

a linear protrusion formed on the substrate for regulating the alignment of the liquid crystal; and

an alignment mark which is formed on the substrate from the same material as that of the linear protrusion and which is used for combining the substrate with the opposite substrate.

13. A liquid crystal display substrate according to claim 12, wherein the protrusion is formed of black resin.

14. A liquid crystal display substrate according to claim 12, wherein the protrusion is formed by laminating a metal layer and a resist layer.

15. A liquid crystal display substrate, comprising:

a substrate which sandwiches a liquid crystal having negative dielectric anisotropy in combination with an opposite substrate provided in a face-to-face relationship therewith;

a linear protrusion formed on the substrate for regulating the alignment of the liquid crystal; and

a frame region which is formed at edges of a display area on the substrate from the same material as that of the linear protrusion and which shields the edges of the display area from

light.

16. A liquid crystal display substrate according to claim 15, wherein the protrusion is formed of black resin.

17. A liquid crystal display substrate according to claim 15, wherein the protrusion is formed by laminating a metal layer and a resist layer.

18. A liquid crystal display comprising two substrates and a liquid crystal sealed between the substrates, wherein a liquid crystal display substrate according to claim 1 is used as at least either of the substrates.

19. A liquid crystal display comprising:

a first substrate having a first resin layer formed thereon;

a second substrate having a second resin layer formed thereon;

a columnar spacer formed as a combination of the first and second resin layers by combining the first and second substrates; and

a liquid crystal sealed between the first and second substrates.

20. A method of manufacturing a substrate for a liquid crystal display, comprising the steps of:

forming a common electrode on a substrate; and

forming an alignment mark on the substrate at the same

time when a linear protrusion is formed on the common electrode.

21. A method of manufacturing a substrate for a liquid crystal display, comprising the steps of:

forming a common electrode on a substrate; and

forming frame region on the substrate at the same time when a linear protrusion is formed on the common electrode.

22. A method of manufacturing a substrate for a liquid crystal display, comprising the steps of:

forming a plurality of bus lines intersecting each other and a thin film transistor on a substrate; and

forming a columnar spacer at the same time when a linear protrusion is formed on the substrate.

23. A liquid crystal display comprising:

a thin film transistor substrate including a first substrate, a plurality of bus lines formed on the first substrate such that they intersect each other, pixel regions defined by the bus lines, a thin film transistor formed in each of the pixel regions, a resin color filter layer formed in each of the pixel regions, and a pixel electrode formed in each of the pixel regions;

a common electrode substrate including a second substrate different from the first substrate in the thickness or material and a common electrode formed on the second substrate, the common electrode substrate being provided in a face-to-face relationship with the first substrate; and

a liquid crystal sealed between the thin film transistor substrate and the common electrode substrate.

24. A liquid crystal display according to claim 23, wherein the second substrate has a thickness smaller than that of the first substrate.
25. A liquid crystal display according to claim 23, wherein the second substrate is lighter than the first substrate.
26. A liquid crystal display according to claim 23, wherein the second substrate is formed from a glass material including alkaline components.
27. A liquid crystal display according to claim 26, wherein the glass material includes 1 % or more alkaline components.
28. A liquid crystal display according to claim 23, wherein the second substrate is formed from a resin material.
29. A liquid crystal display according to claim 23, further comprising a columnar spacer for maintaining a gap between the thin film transistor substrate and the common electrode substrate.
30. A liquid crystal display according to claim 23, wherein the thin film transistor substrate is located closer to a display side.
31. A liquid crystal display according to claim 30, wherein at least surfaces of the bus lines facing the first substrate

is formed from a low reflection material.

32. A liquid crystal display according to claim 30, wherein at least surfaces of a drain electrode and a source electrode of the thin film transistor facing the first substrate are formed from a low reflection material.

33. A liquid crystal display substrate, comprising:

- a substrate which sandwiches a liquid crystal in combination with an opposite substrate provided in a face-to-face relationship therewith;

- a plurality of gate bus lines formed on the substrate;

- a plurality of drain bus lines formed on the substrate such that they intersect the gate bus lines;

- pixel regions defined by the gate bus lines and the drain bus lines;

- a thin film transistor formed in each of the pixel regions;

- a resin color filter layer formed in each of the pixel regions;

- a pixel electrode formed in each of the pixel regions;

and

- a resin layer formed to cover source and drain electrodes of the thin film transistor and the drain bus lines.

34. A liquid crystal display substrate according to claim 33, wherein the resin layer is constituted by the resin color filter layer.

35. A liquid crystal display substrate according to claim 34,

wherein a resin color filter layer in a different color is laminated on the resin layer.

36. A liquid crystal display substrate according to claim 33, wherein the resin layer includes black resin.

37. A liquid crystal display substrate according to claim 33, wherein the resin layer includes a layer to form a columnar spacer.

38. A liquid crystal display comprising two substrates and a liquid crystal layer sealed between the substrates, a substrate for a liquid crystal display according to claim 33 being used as either of the substrates.

39. A liquid crystal display according to claim 38, wherein a polymeric structure is formed in the liquid crystal layer.

40. A method of manufacturing a substrate for a liquid crystal display, comprising the steps of:

forming a thin film transistor on a substrate;

forming a first resin color filter layer such that the color filter layer covers source and drain electrodes of the thin film transistor and a drain bus line;

forming a second resin color filter layer in another pixel region; and

forming a third resin color filter layer in still another pixel region.